



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815**

Serial No.: **09/987,012**

Examiner: **Chris C. Chu**

Filed: **November 13, 2001**

P.T.O. Confirmation No.: 6363

For. **SEMICONDUCTOR WAFER DEVICE HAVING SEPARATED CONDUCTIVE PATTERNS IN PERIPHERAL AREA AND ITS MANUFACTURING METHOD**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: February 25, 2005

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated November 1, 2004 for one month, from February 1, 2005 to March 1, 2005.

Attached please find a check in the amount of \$910.00 to cover the cost of the extension (\$120.00) and to cover the cost of the RCE (\$790.00) for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

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